

FORM PTO-1449 U.S. Department of Commerce
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ATTY. DOCKET NO.

SERIAL NO.

500.31108CC5

09/712,171

APPLICANT

A. TANAKA et al.

FILING DATE

November 15, 2000

GROUP

2184

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

MS	G. Gibson et al., "Failure Correction Techniques for Large Disk Arrays", <u>Proceedings of the Third International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS-III)</u> , April 3-6, 1989, Boston, MA, pp. 123-132, ACM Press, New York, NY, ISBN 0-89791-300-0.

EXAMINER

Scott Baderman

DATE CONSIDERED

12/9/02

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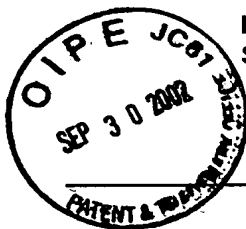
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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[illegible]

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AS	T. Olson, "Disk Array Performance in a Random IO Environment", <u>ACM SIGARCH Computer Architecture News</u> , Vol. 17, No. 5, September 1989, pp. 71-77, ACM Press, New York, NY, ISSN 0163-5964.
↓	P. Chen et al., "Maximizing Performance in a Striped Disk Array", <u>Proceedings of The 17th Annual International Symposium on Computer Architecture</u> , May 28-31, 1990, Seattle, WA, pp. 322-331, IEEE Computer Society Press, Los Alamitos, CA.

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